FIG. 1

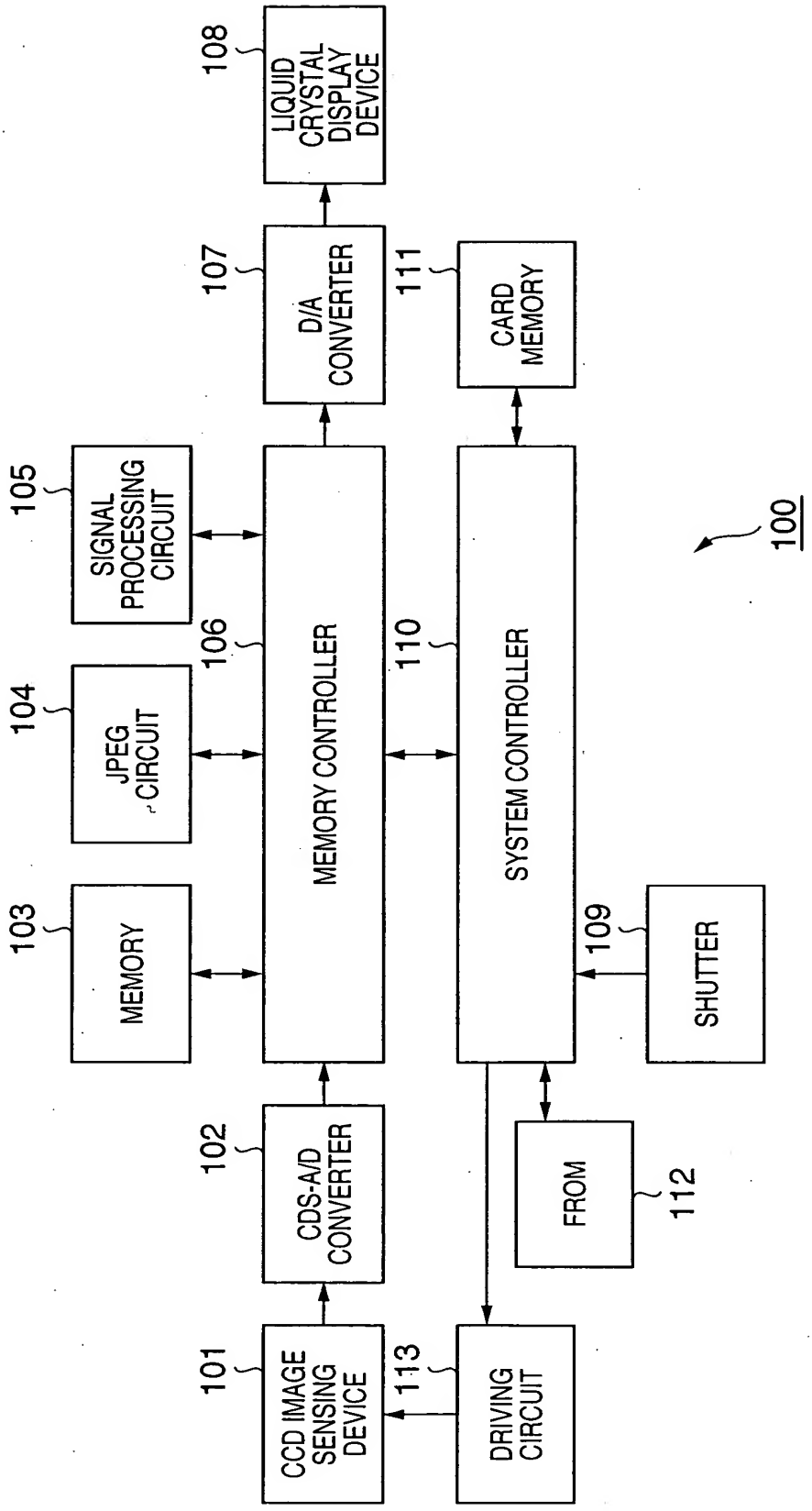
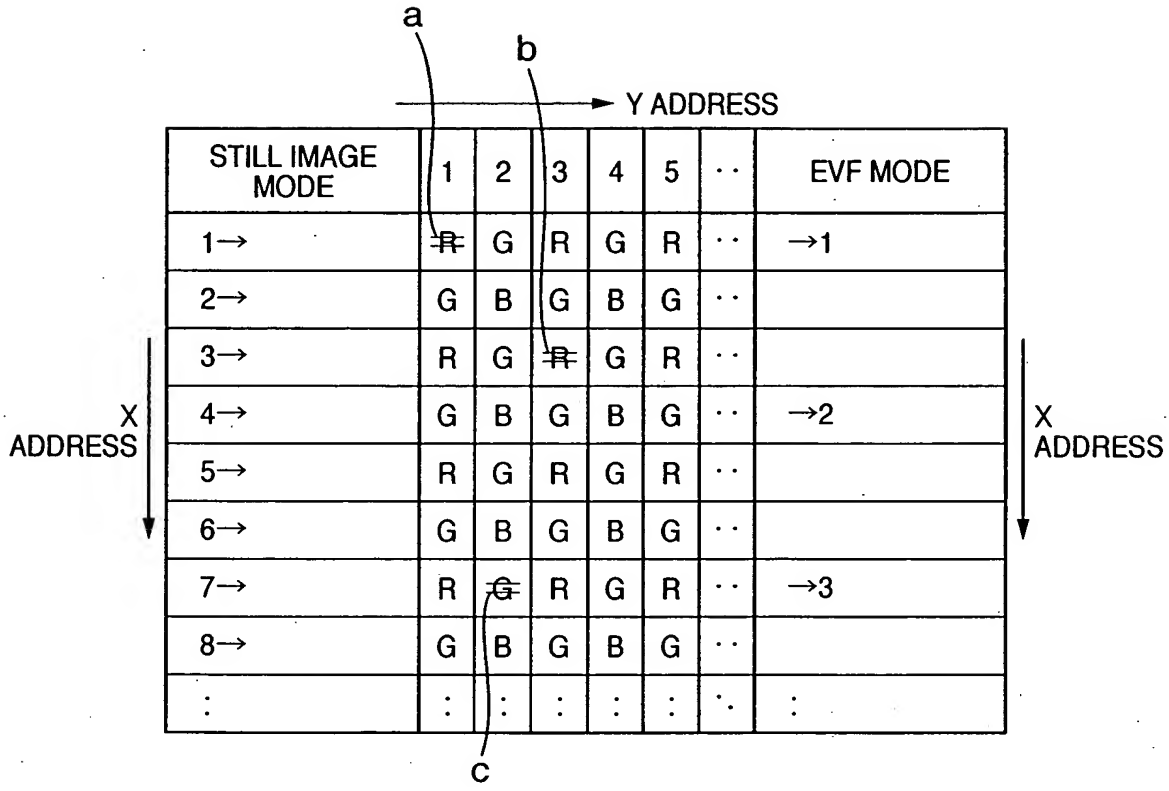

100
101
CCD IMAGE SENSING DEVICE
102
CDS-A/D CONVERTER
103
MEMORY
104
JPEG CIRCUIT
105
SIGNAL PROCESSING CIRCUIT
106
MEMORY CONTROLLER
107
D/A CONVERTER
108
LIQUID CRYSTAL DISPLAY DEVICE
109
SHUTTER
110
SYSTEM CONTROLLER
111
CARD MEMORY
112
FROM
113
DRIVING CIRCUIT

FIG. 2



STILL IMAGE MODE	1	2	3	4	5	··	EVF MODE
1→	~~R~~	G	R	G	R	··	→1
2→	G	B	G	B	G	··	
3→	R	G	~~R~~	G	R	··	
4→	G	B	G	B	G	··	→2
5→	R	G	R	G	R	··	
6→	G	B	G	B	G	··	
7→	R	~~G~~	R	G	R	··	→3
8→	G	B	G	B	G	··	
:	:	:	:	:	:	⋱	:

~~G~~, ~~R~~ : DEFECTIVE PIXEL

FIG. 3A

X ADDRESS	Y ADDRESS
1	1
3	3
7	2

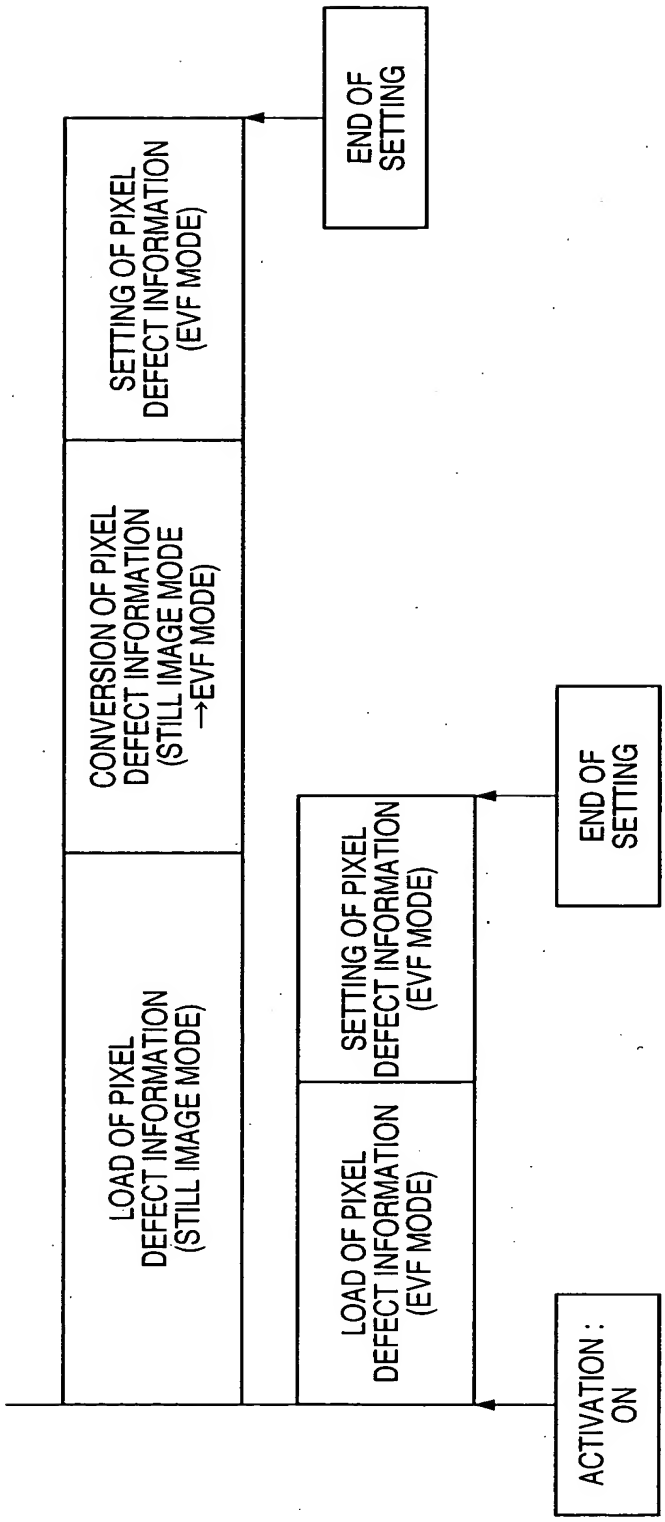
STILL IMAGE MODE

FIG. 3B

X ADDRESS	Y ADDRESS
1	1
3	2

EVF MODE

FIG. 4


LOAD OF PIXEL DEFECT INFORMATION (STILL IMAGE MODE)
CONVERSION OF PIXEL DEFECT INFORMATION (STILL IMAGE MODE →EVF MODE)
SETTING OF PIXEL DEFECT INFORMATION (EVF MODE)
END OF SETTING
LOAD OF PIXEL DEFECT INFORMATION (EVF MODE)
SETTING OF PIXEL DEFECT INFORMATION (EVF MODE)
END OF SETTING
ACTIVATION : ON